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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Shinsuke OKADA et al.

Application No.: 10/580,436

Filed: May 24, 2006

Docket No.: 128096

For: RECTIFYING DEVICE, ELECTRONIC CIRCUIT USING THE SAME, AND METHOD  
OF MANUFACTURING RECTIFYING DEVICE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- ☒ 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- ☒ 2. One or more reference cited herein was cited in a counterpart foreign application. An English language version of the foreign search report is attached for the Examiner's information. See References 3-14.
- ☒ 3. An English language Abstract of one or more non-English language reference is attached hereto. See References 3-12.

- ☒ 4. A computer-generated English language translation of one or more Japanese Patent Publication cited herein has been obtained from the website of the Japanese Patent Office ([<http://www.jpo.go.jp>]), and is attached, but has not been reviewed for accuracy. See References 3,6,7 and 9-12
- ☒ 5. Reference 1 corresponds to reference 3.  
Reference 2 corresponds to reference 6.

Respectfully submitted,



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<p>DEPOSIT ACCOUNT USE AUTHORIZATION Please grant any extension necessary for entry; Charge any fee due to our Deposit Account No. 15-0461</p>
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Sheet 1 of 1

Form PTO-1449 (REV. 1/06)		US Dept. of Commerce PATENT & TRADEMARK OFFICE		ATTY DOCKET NO. 128096		APPLICATION NO. 10/580,436	
INFORMATION DISCLOSURE STATEMENT  (Use several sheets if necessary)				APPLICANT(S) Shinsuke OKADA et al.			
				FILING DATE May 24, 2006		GROUP	
U.S. PATENT DOCUMENTS							
Examiner Initials	Cite No.	Document Number	Date	Name			
	1	2002/0172639 A1	11/21/2002	Horiuchi et al.			
	2	6,340,822 B1	01/22/2002	Broen et, al.			
FOREIGN PATENT DOCUMENTS							
Examiner Initials	Cite No.	Document Number	Date	Country	With English Abstract	With English Translation	
	3	JP A 2002-346996	12/4/2002	Japan	X	X	
	4	JP A 58-98333	06/11/1983	Japan	X		
	5	JP A 59-32166	02/21/1984	Japan	X		
	6	JP A 2002-141633	05/17/2002	Japan	X	X	
	7	JP A 2003-309265	10/31/2003	Japan	X	X	
	8	WO 2002/063693 A1	08/15/2002	WIPO	X		
	9	JP A 2002-076324	03/15/2002	Japan	X	X	
	10	JP A 6-29556	02/04/1994	Japan	X	X	
	11	JP A 6-29514	02/04/1994	Japan	X	X	
	12	JP A 2003-258164	09/12/2003	Japan	X	X	
OTHER DOCUMENTS							
Examiner Initials	Cite No.	(Including Author, Title, Date, Pertinent Pages, etc.)					
	13	YAMADA, T.; (2002); "Modeling of kink-shaped carbon-nanotube Schottky diode with gate bias modulation"; APPLIED PHYSICS LETTERS, 80(21); pp. 4027-4029					
	14	FREITAGE, M., et al.; (2001); "Controlled creation of a carbon nanotube diode by a scanned gate"; APPLIED PHYSICS LETTERS, 79(20); pp. 3326-3328					
EXAMINER					DATE CONSIDERED		
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

Date: September 5, 2006